



Lesson 2: Real Components

In this presentation, the real behavior of passive devices in the field of electromagnetic compatibility will be analyzed.

They will therefore cease to be considered ideal elements.



Maxwell Eq.

$$\text{rot } \vec{E} = -\frac{\partial \vec{B}}{\partial t}$$

$$\text{div } \vec{D} = \rho$$

$$\text{rot } \vec{H} = \vec{J} + \frac{\partial \vec{D}}{\partial t}$$

$$\text{div } \vec{B} = 0$$

$$\vec{J} = \sigma \cdot \vec{E}$$

Matter Eq.

$$\vec{D} = \epsilon \cdot \vec{E}$$

$$\vec{B} = \mu \cdot \vec{H}$$

$$\vec{J} = \sigma \cdot \vec{E}$$

+ Boundary conditions



Solution:

$$\vec{D}, \vec{E}, \vec{B}, \vec{H}$$

Maxwell's equations allow to determine the existing electromagnetic fields depending on the properties of the materials



From Maxwell equations

$$L = \frac{\Phi}{I} = \frac{\mu \cdot \iint_S \vec{H} \cdot d\vec{s}}{\oint_c \vec{H} \cdot d\vec{l}}$$

$$C = \frac{Q}{V} = \frac{\int_v \rho \cdot dv}{\int \vec{E} \cdot d\vec{l}} = \frac{\varepsilon \cdot \int \text{div } \vec{E} \cdot dv}{\int \vec{E} \cdot d\vec{l}} = \frac{\varepsilon \iiint_V \vec{E} \cdot d\vec{s}}{\int \vec{E} \cdot d\vec{l}}$$

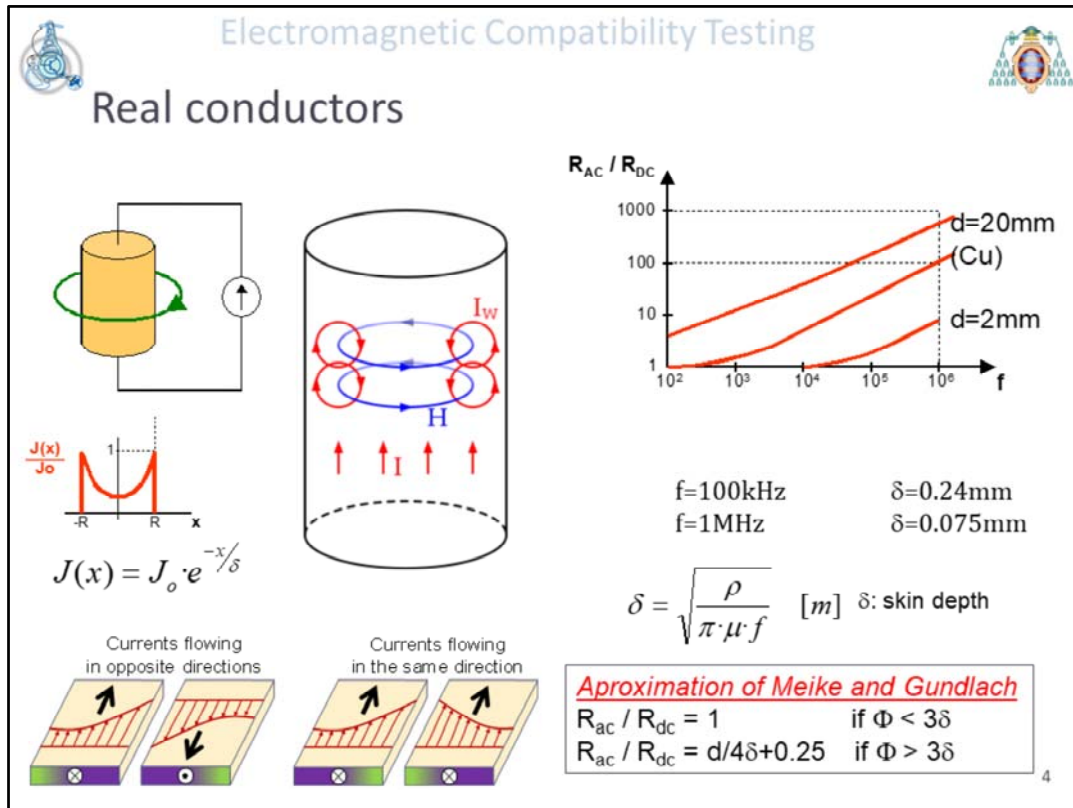
$$\vec{J} = \sigma \cdot \vec{E} \Rightarrow V = \int \frac{\vec{J}}{\sigma} \cdot d\vec{l} = I \cdot \int \frac{d\vec{l}}{S \cdot \sigma} = R \cdot I$$

Ohm Law

$$fem = i(t) \cdot R + \frac{1}{C} \cdot \int i(t) \cdot dt + L \cdot \frac{\partial i(t)}{\partial t}$$

2^a Kirchhoff law

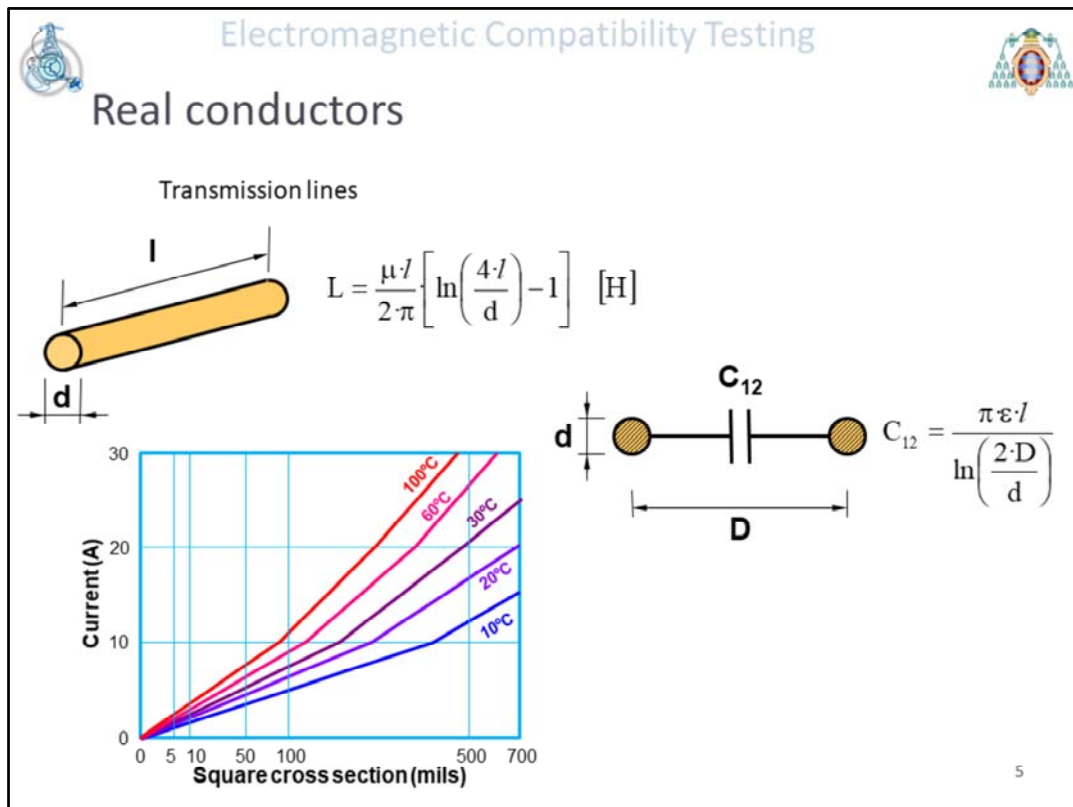
Once the existing electromagnetic field distribution is known, the energy due to the magnetic field can be associated with the L parameter (inductance), that due to the electric field can be linked to parameter C (capacity) and heat dissipation in a resistance R can be evaluated through Ohm's law.



One of the phenomena that appear in real drivers is the skin effect. The existence of alternating fields caused by the alternating current that passes through a conductor causes the induction of currents in the conductor itself, giving rise to a redistribution of the current on the surface of the conductor.

This effect is more visible when the frequency increases, giving rise to an increase in resistance with frequency.

This effect is quantified by the parameter δ (*delta*), also known as depth skin.



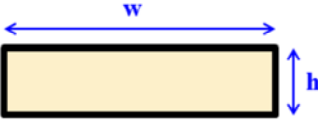
To characterize a real conductor it is necessary to take into account its inductance per unit length or parasitic capacity with other conductors. On the other hand there is a limit as to the current density in a conductor for a given temperature.



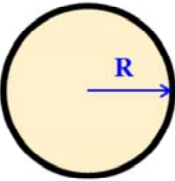
Electromagnetic Compatibility Testing



Inductance



$$L_{rect} = N^2 \cdot \frac{\mu_r \cdot \mu_0}{\pi} \cdot \left[-2 \cdot (w+h) + 2 \cdot \sqrt{h^2 + w^2} - h \cdot \ln \left(\frac{h + \sqrt{h^2 + w^2}}{w} \right) - w \cdot \ln \left(\frac{w + \sqrt{h^2 + w^2}}{h} \right) + h \cdot \ln \left(\frac{2 \cdot h}{a} \right) + w \cdot \ln \left(\frac{2 \cdot w}{a} \right) \right] \quad [H]$$



$$L_{circle} \approx N^2 \cdot R \cdot \mu_r \cdot \mu_0 \cdot \left[\ln \left(\frac{8 \cdot R}{a} \right) - 2.0 \right] \quad [H]$$

$$L_{trace-pair} \approx \frac{\mu_r \cdot \mu_0 \cdot h}{w} \quad [H/m]$$



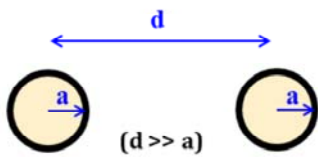
$(w \gg h ; h > t)$

Depending on the geometry there are several formulas that the inductance per unit length to be calculated

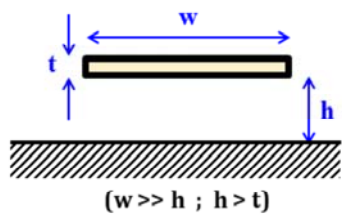


Electromagnetic Compatibility Testing

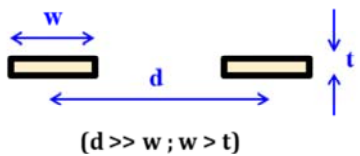




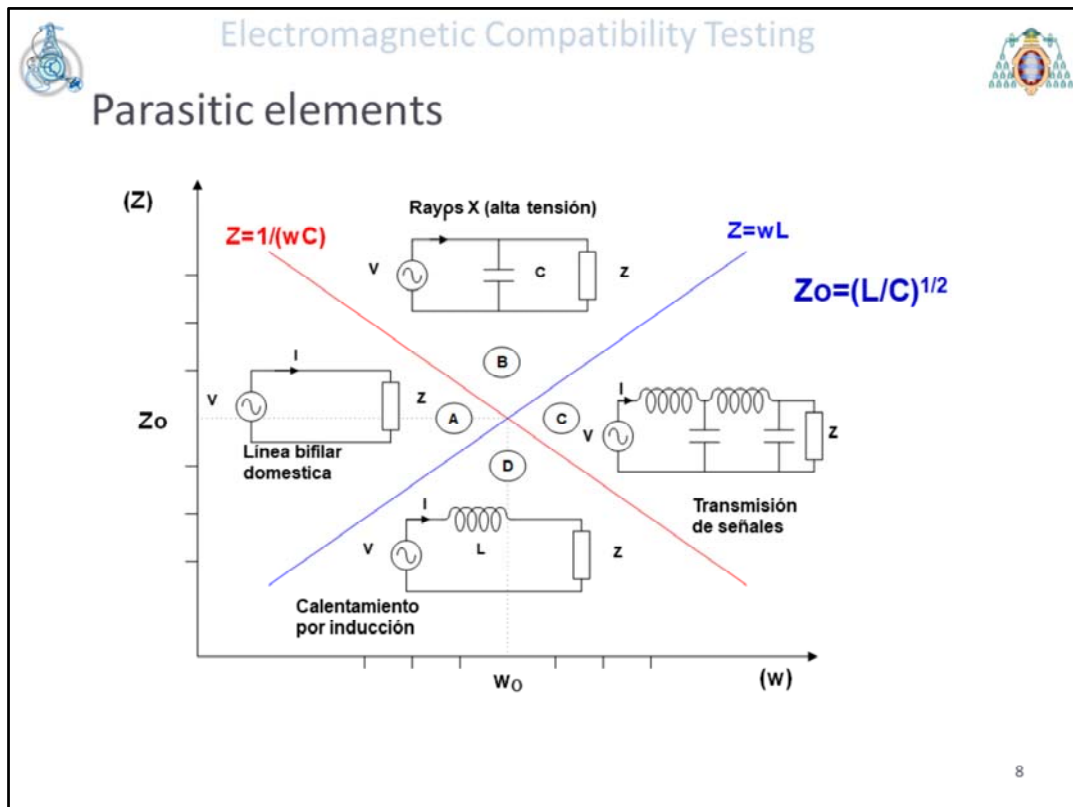
$$L_{\text{wire-pair}} \approx \frac{\mu_r \cdot \mu_0}{\pi} \cdot \cosh^{-1} \left(\frac{d}{2 \cdot a} \right) \quad [H/m]$$



$$L_{\text{trace-ground}} \approx \frac{\mu_r \cdot \mu_0 \cdot h}{w} \quad [H/m]$$



$$L_{\text{trace-trace}} \approx \frac{\mu_r \cdot \mu_0}{\pi} \cdot \cosh^{-1} \left(\frac{d}{w} \right) \quad [H/m]$$



Traditionally, when the working frequency is low (50Hz) and the current or voltage is also low, we can neglect the parasitic elements due to the magnetic (inductance) and electric (capacitance) fields; section "A". If the voltage or current is high (Sections B and D respectively) the electric and magnetic fields will be high and parasitic capacitance and inductance can not be ignored. Finally, if the frequency is high (communication systems), distributed parameters will be used (section C)

Electromagnetic Compatibility Testing

$$L = \frac{\mu \cdot l}{\pi} \left[\ln \left(\frac{2 \cdot D}{d} \right) - \frac{D}{l} \right] \quad [\text{H}]$$

Poynting vector:

$$\mathbf{P} = \iint \mathbf{P}_y \cdot d\mathbf{S} \quad \text{where} \quad P_y = \vec{E} \times \vec{H}$$

electromagnetic energy transfer per unit area per unit time

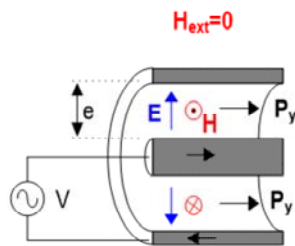
By calculating the Poynting vector, it is possible to identify how the transmission of power from the primary source to the load in an electrical system is carried out.



Electrical energy delivered to the load is flowing entirely through the dielectric

$H_{ext}=0$, there is no net current in the outside

$$\oint \vec{H} \cdot d\vec{l} = I = 0$$



$$H = \frac{I}{2 \cdot r \cdot \pi}$$

$$E = \frac{V}{e}$$

$$P_y = E \times H = \frac{V}{e} \cdot \frac{I}{2 \cdot \pi r} \quad P = \iint P_y \cdot dS = P_y \cdot S = P_y \cdot (e \cdot 2 \cdot \pi \cdot r)$$

$$P = \left(\frac{V}{e} \frac{I}{2 \pi r} \right) (e 2 \pi r) = V \cdot I$$

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In the case of a coaxial cable, the calculation of the transferred power can be made by evaluating the magnetic and electric fields, obtaining the traditional formula of $P = V \cdot I$

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Propagation time

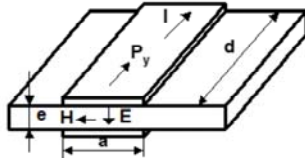
$$t = \frac{d}{v} = d \cdot \left(\left(\frac{1}{2} \epsilon \frac{E}{H} \right) + \left(\frac{1}{2} \mu \frac{H}{E} \right) \right)$$

$$(E/H) = (a/e) \cdot (V/I)$$

$$t = \frac{d}{2} \cdot \sqrt{\epsilon \cdot \mu} \cdot \left(X + \frac{1}{X} \right)$$

$$X = \frac{a}{e} \cdot \frac{V}{I} \cdot \sqrt{\frac{\epsilon}{\mu}}$$

Minimum t



$$\frac{dt}{dX} = 0 \Rightarrow X = 1 \Rightarrow \frac{a}{e} = (I/V) \cdot \sqrt{\frac{\mu}{\epsilon}}$$

$$L = \mu \cdot d \cdot (e/a)$$

$$C = \epsilon \cdot d \cdot (a/e)$$

$$(L/C)^{1/2} = (e/a) \cdot (\mu/\epsilon)^{1/2}$$

$$(L \cdot C)^{1/2} = d \cdot (\mu \cdot \epsilon)^{1/2}$$

$$H \cdot a = I, E \cdot e = V \Rightarrow E/H = (V/I) \cdot (a/e)$$

$$(a/e) = (\mu/\epsilon)^{1/2} \cdot (C/L)^{1/2} = (E/H) \cdot (I/V)$$

$$\sqrt{\frac{L}{C}} = \frac{V}{I}$$

$$\frac{E}{H} = \sqrt{\frac{\mu}{\epsilon}}$$

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To minimize propagation time, the impedance of the material must equal the wave impedance.



Electromagnetic Compatibility Testing



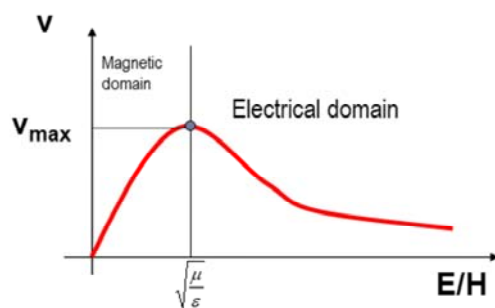
$$\mathbf{H} \cdot \mathbf{a} = I, \quad \mathbf{E} \cdot \mathbf{e} = V \Rightarrow E/H = (\mu/\epsilon)^{1/2}$$

$$(V/e) \cdot (a/I) = (\mu/\epsilon)^{1/2}$$

$$(\mu/\epsilon)^{1/2} \quad \text{material impedance}$$

$$(L/C)^{1/2} \quad \text{line impedance}$$

$$(E/H) \quad \text{wave impedance}$$





Electromagnetic Compatibility Testing



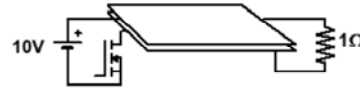
PCB ($\epsilon_r=4.5$) , de 100 W, 10 V, 10 A:

$$(V/I)=1 \Omega$$

$$(\mu/\epsilon)^{1/2}=377/4.5^{1/2}=178 \Omega$$

Maximum speed if

$$\frac{E}{H} = \sqrt{\frac{\mu}{\epsilon}} = 178 \Omega$$



$$\frac{a}{e} = (I/V) \cdot \sqrt{\frac{\mu}{\epsilon}} = \frac{178}{1} \Rightarrow \begin{matrix} e=0.2 \text{ mm} \\ a=36 \text{ mm} \end{matrix}$$

Propagation speed:

$$v = \frac{1}{\sqrt{\mu \cdot \epsilon}} = 141 \cdot 10^6 \text{ [m/s]}$$

If we use a MOSFET with 3ns transient response
It would be desirable to have at least
the same propagation time. Therefore the maximum
Track length:

Track length

$$d=v \cdot t=141 \cdot 10^6 \text{ m/s} \cdot 3 \text{ ns}=40 \text{ cm}$$

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In this example, the length of a track is calculated to achieve a propagation time of 3ns



Design connections with PCB's

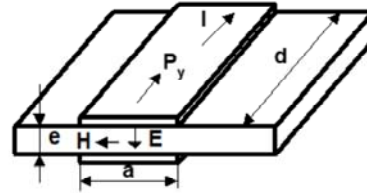
Minimum propagation time is obtained if:

$$\frac{a}{e} \cdot \frac{V}{I} = \sqrt{\frac{\mu}{\epsilon}}$$

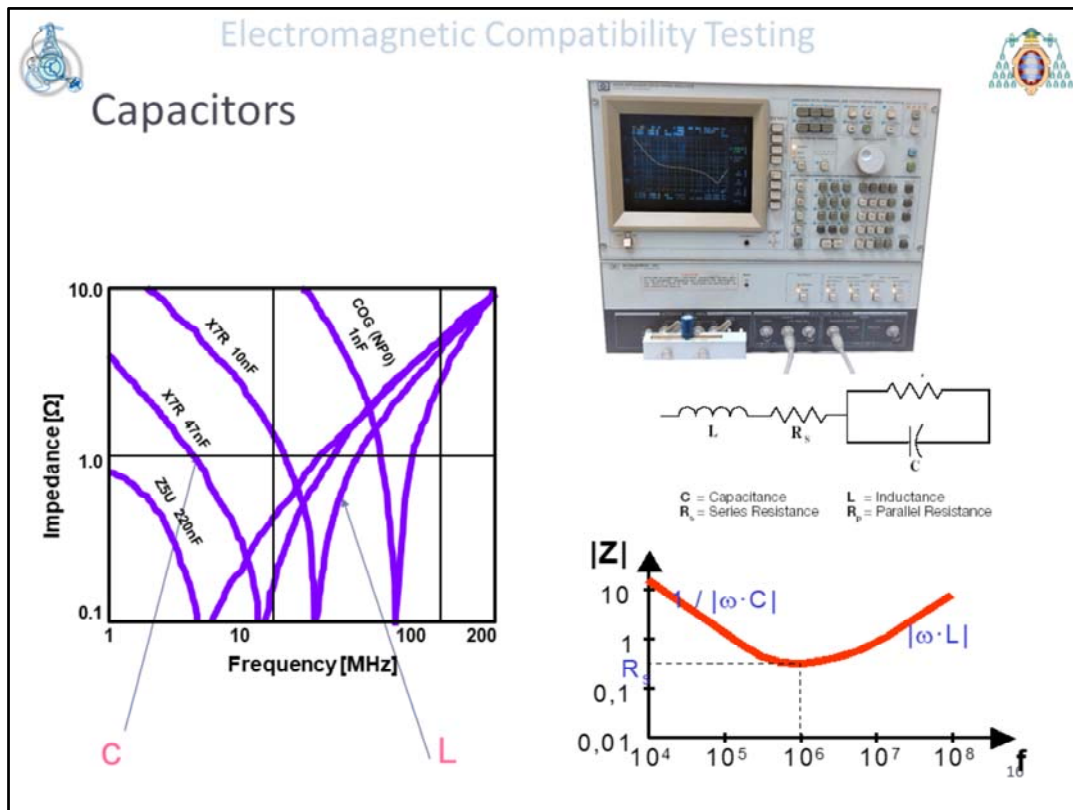
Once the materials are selected

If (V/I) is low $\Rightarrow (a/e) \uparrow$

Ground plane is the solution

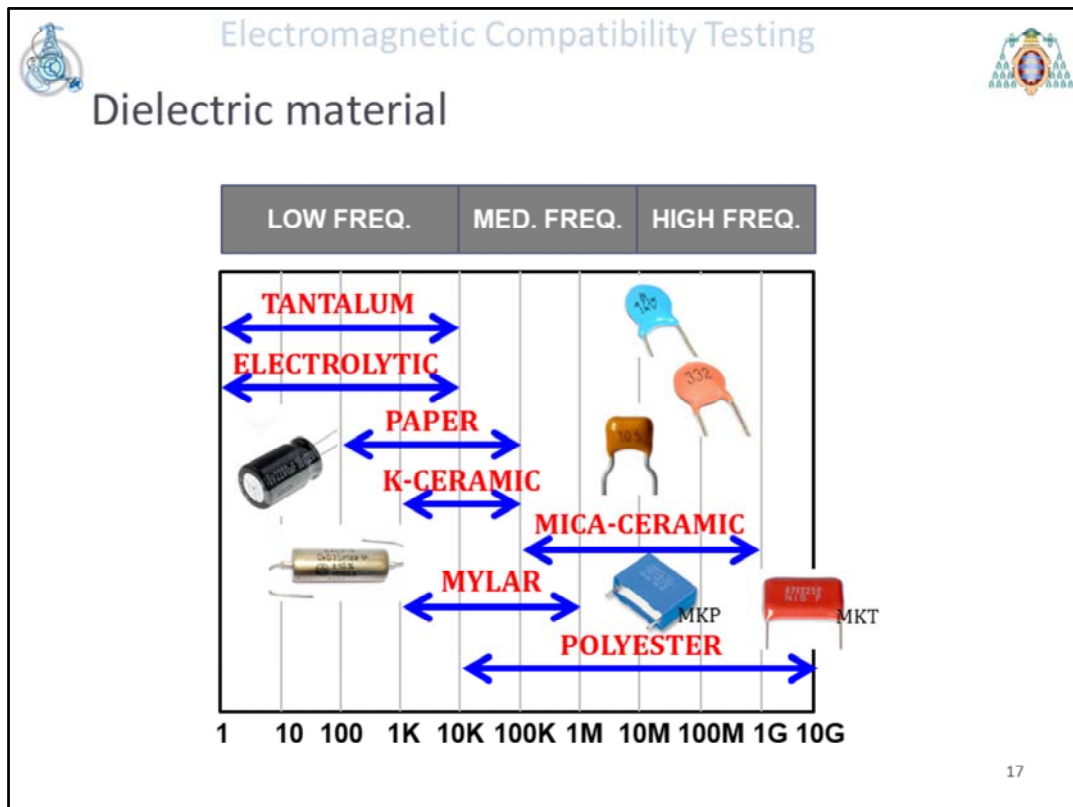


It can be observed that, to obtain high propagation times, the width of the tracks must also be large, the ground plane being a highly recommended solution

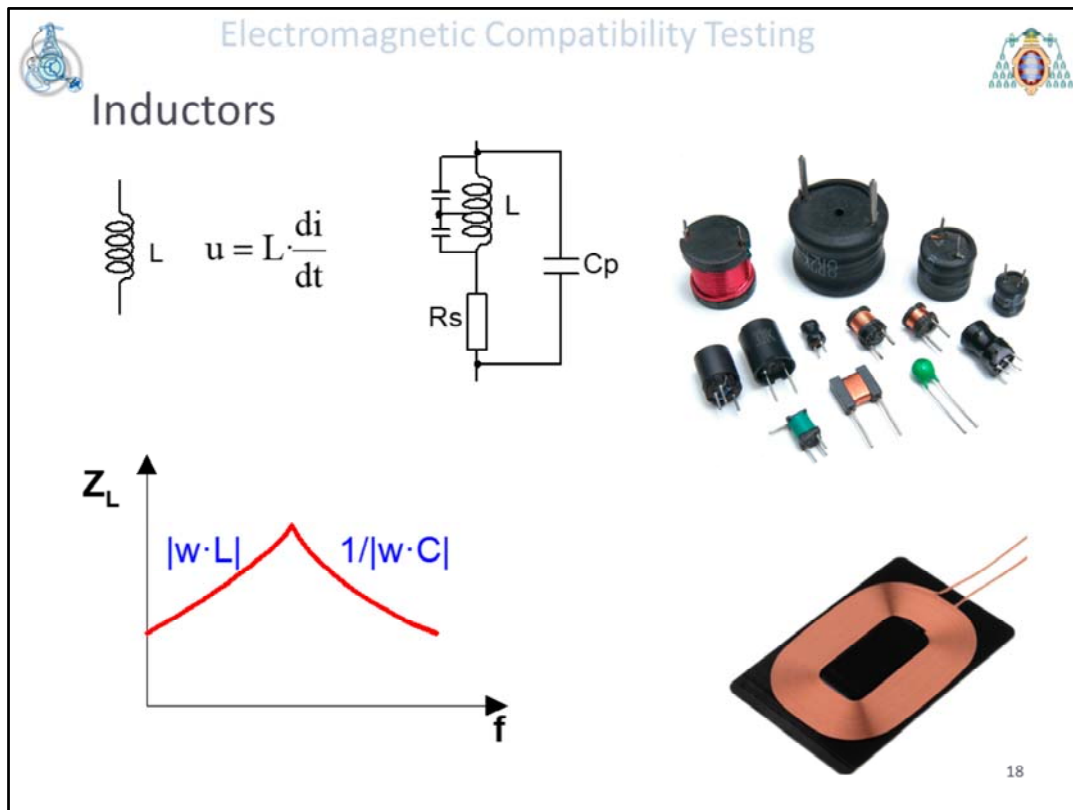


If we analyze the behavior of a real capacitor as a function of frequency, it is observed that after a certain frequency the impedance of the capacitor increases.

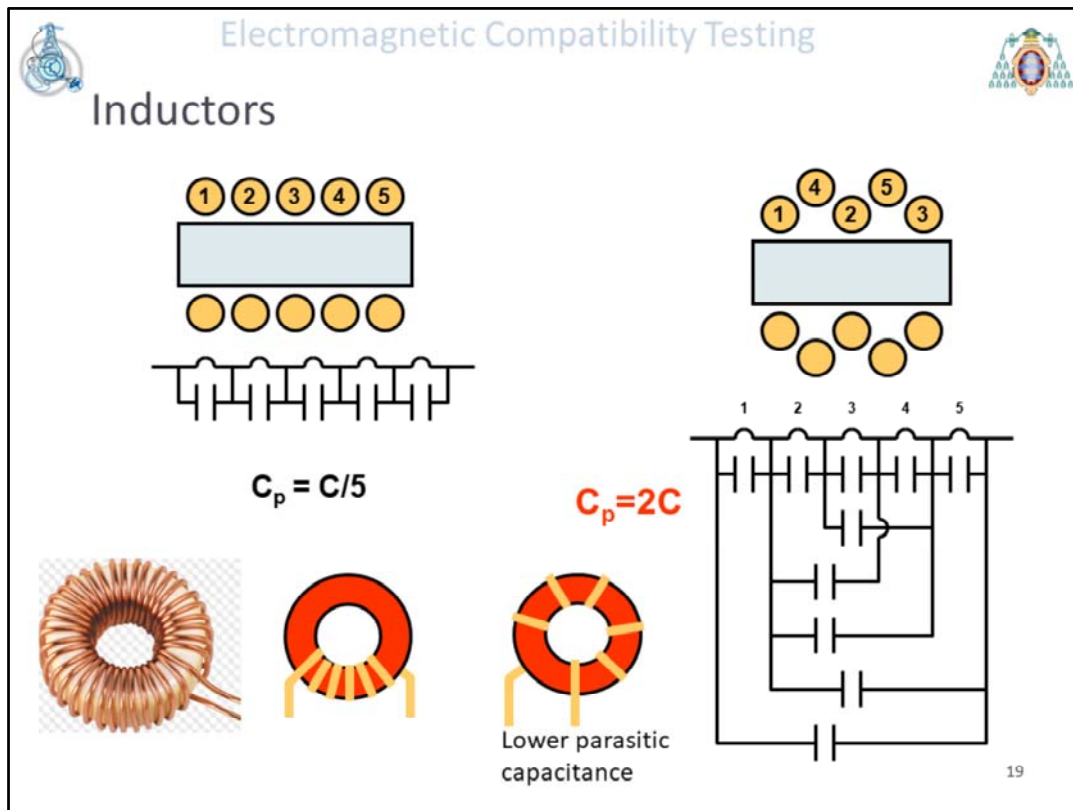
This phenomenon is due to the parasitic inductance that we must place in series with the capacitor.



The selection of the type of capacitor based on the materials used in its manufacture and the frequency of work to which it will be subjected, is a critical aspect. Thus electrolytic capacitors should not be used in high-frequency applications due to their high parasitic inductance. The graph shows the use of different technologies.



Because of the way coils are actually constructed, their impedance begins to decrease after a certain frequency. This is due to the presence of parasitic capacitance between the windings.

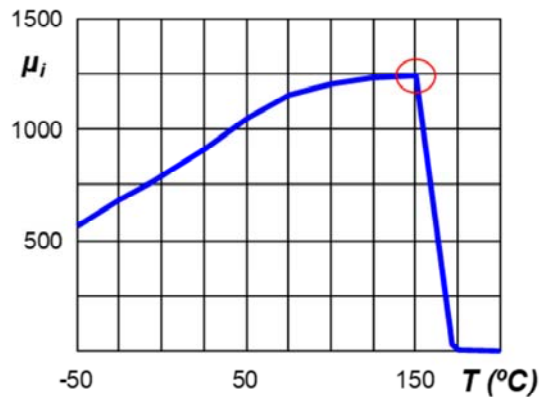


By increasing the distance between conductors, the parallel parasitic capacitance of the equivalent model is minimized.



Power inductors

The ferrites used to produce power inductors lose their permanent magnetic properties above a certain temperature called the Curie temperature (TC), or Curie point.



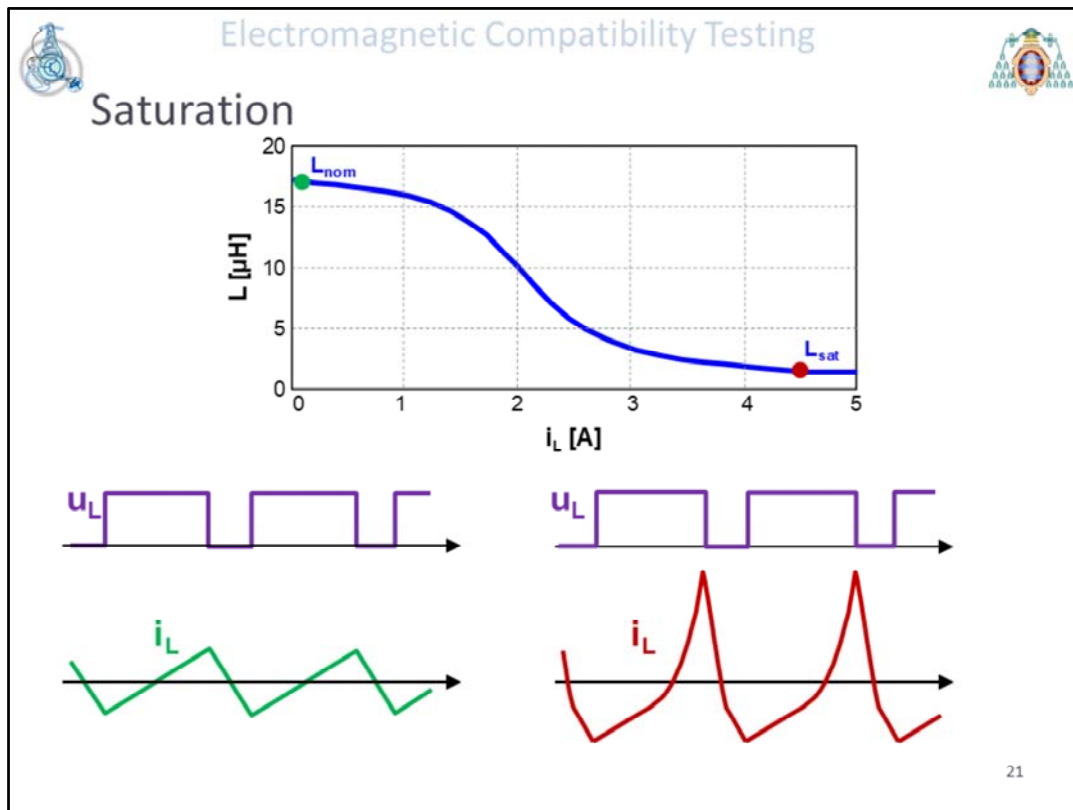
Power inductors define a maximum current for a fixed temperature increase ΔT

Rated current (max.)

$I_R = 2.00 \text{ A}$ (for $\Delta T = 40\text{K}$)

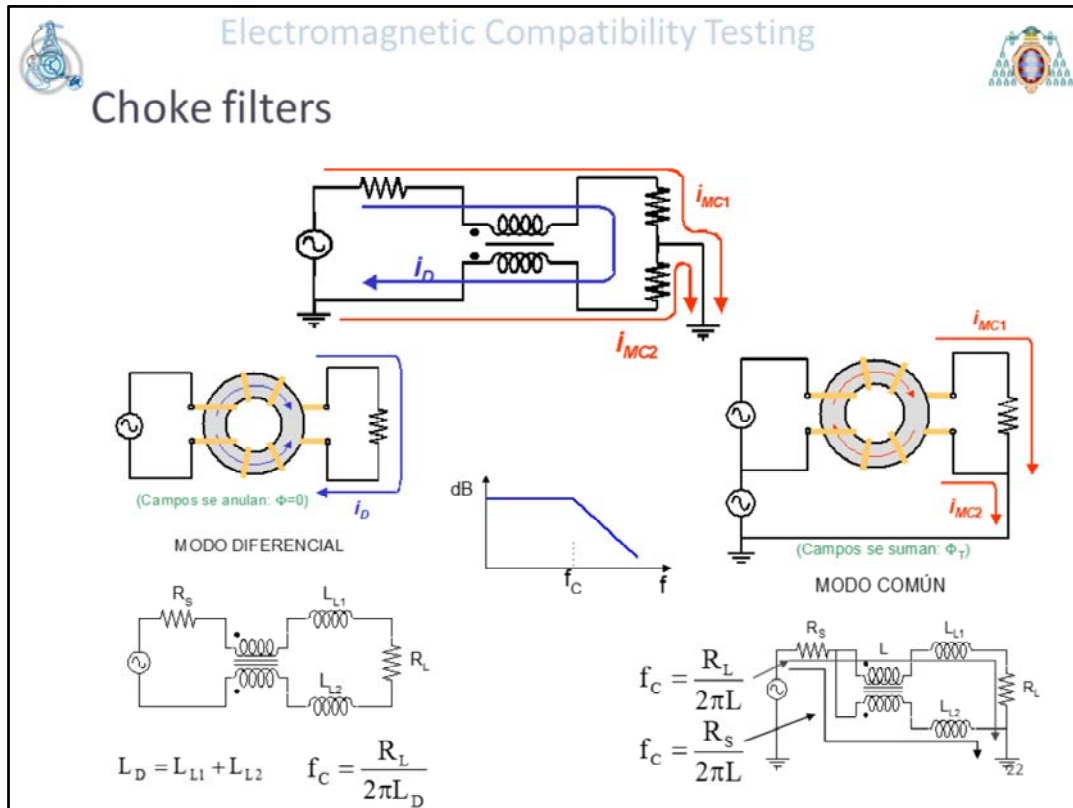
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Another parameter to take into account is the temperature at which the magnetic material loses its magnetic properties (Curie Temperature).



When a coil is subjected to high currents, the magnetic material can reach saturation and this causes a significant reduction in the value of the equivalent inductance, which contributes in many cases, to the current increasing even more.

The figure on the right shows the current when the coil is saturated



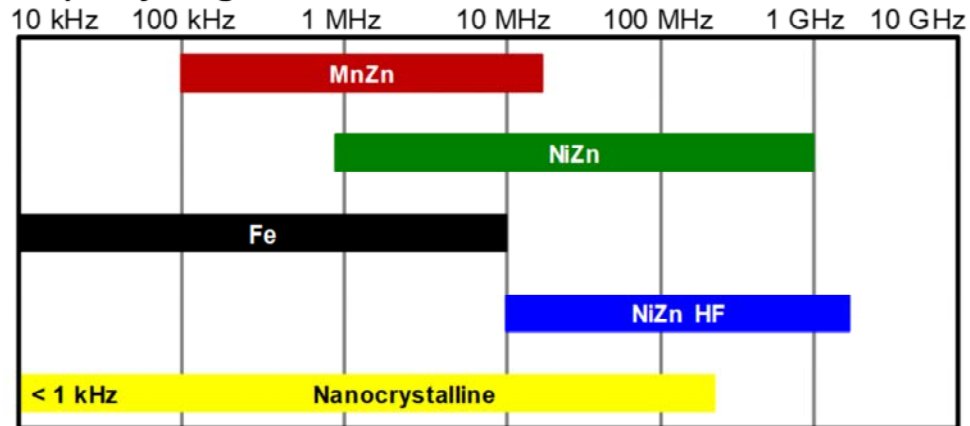
A type of coupled coils is known as a "choke filter". This type of filter is used primarily to reduce common-mode currents. The currents in differential mode, since they create flows in opposite directions in the magnetic material, are not affected nor can they saturate the magnetic core of these devices.



Choke filters

Do not use chokes as inductors: they can be saturated with very low current

Frequency Range



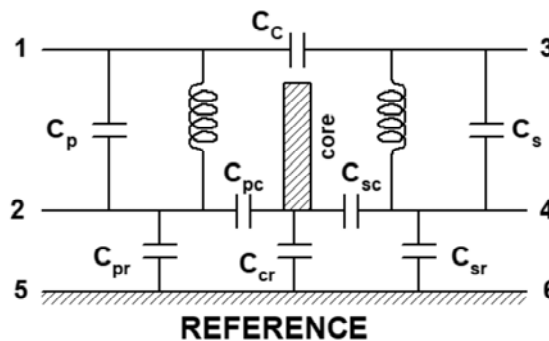
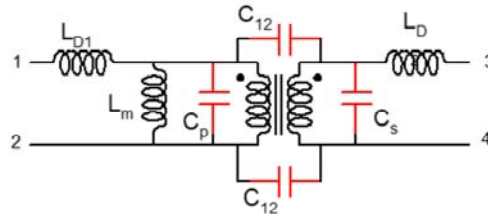
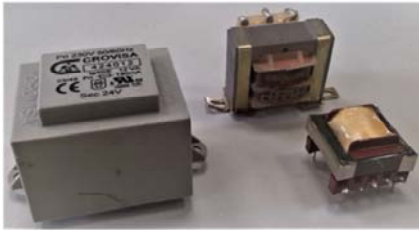
Source: Würth Elektronik

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There are different types of magnetic materials. Depending on the frequency at which they will be used, the appropriate material must be selected.

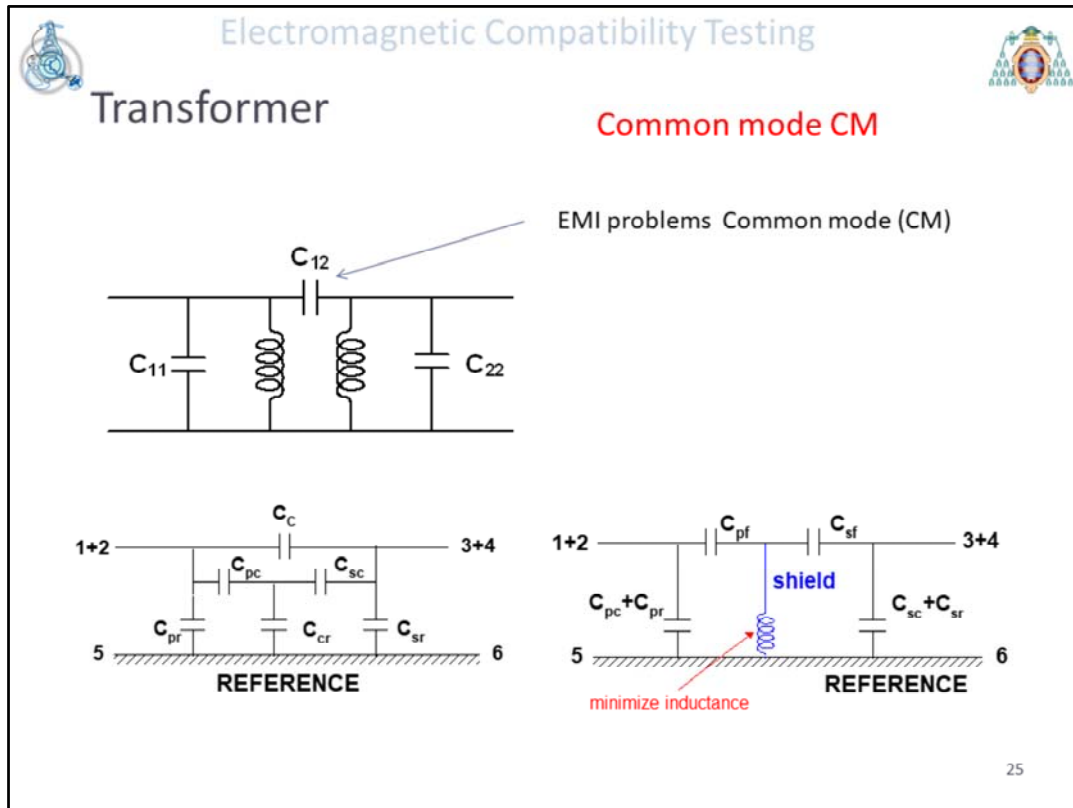


Transformer

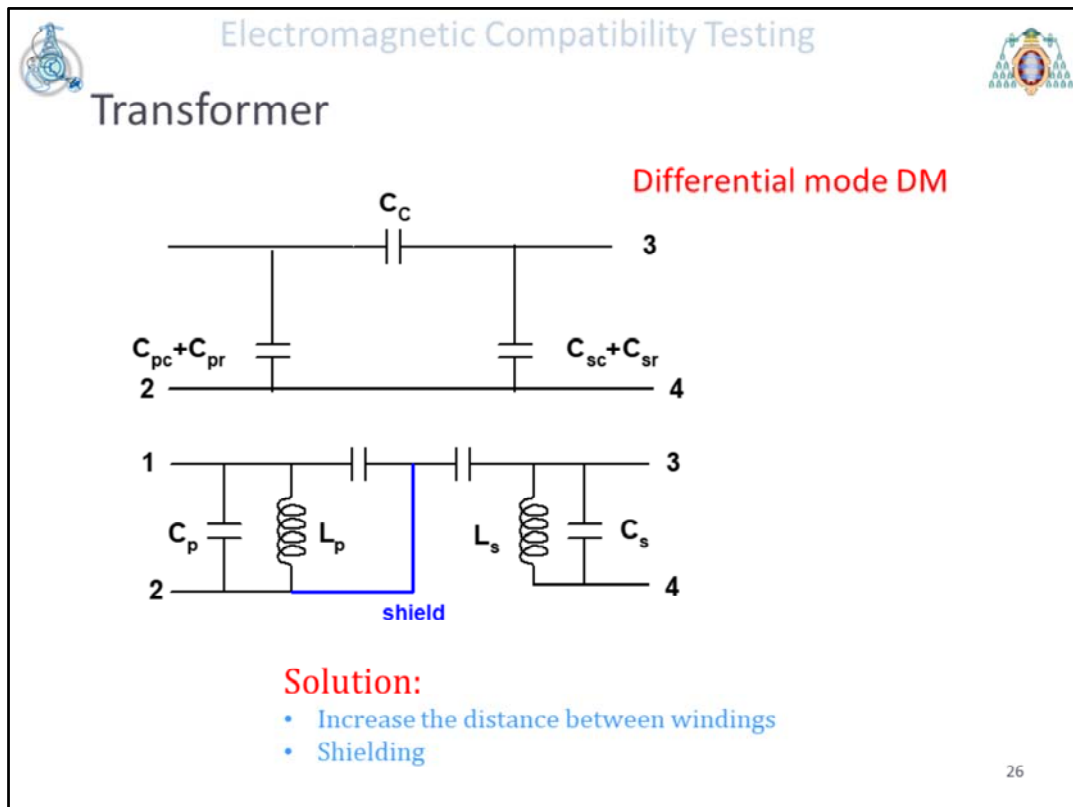


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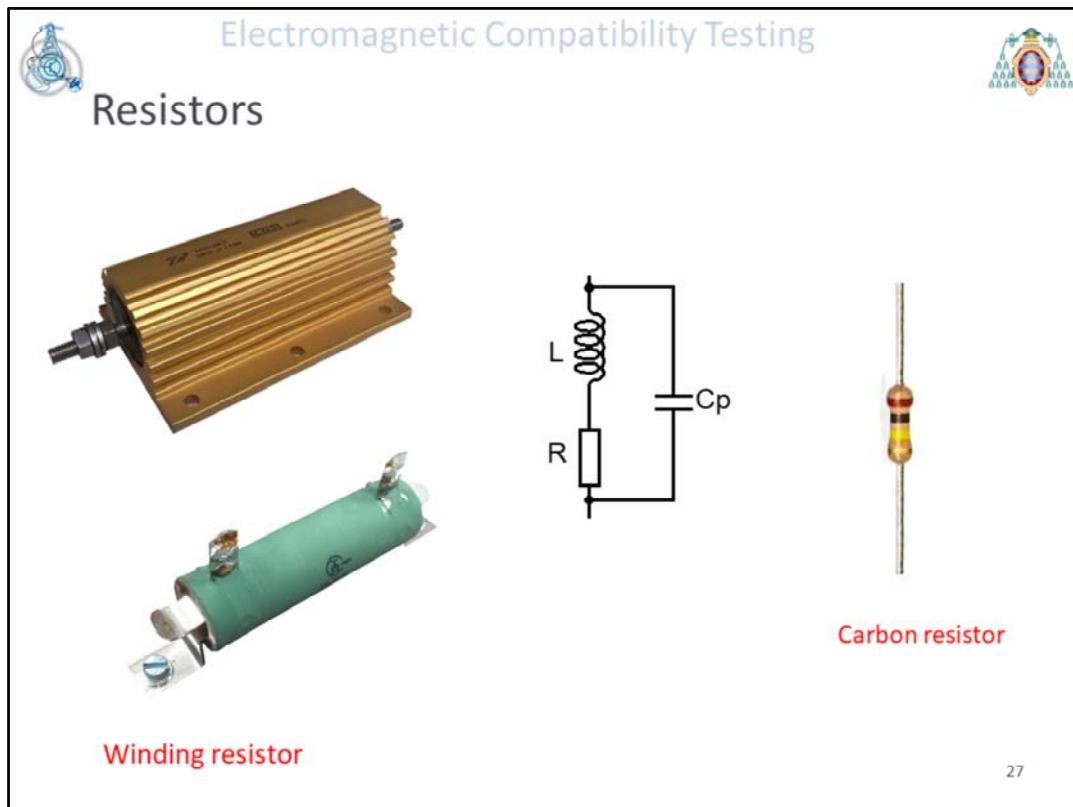
Although transformers introduce galvanic isolation between primary and secondary, when it comes to high-frequency signals, the existing parasitic capacitances may short-circuit both windings.



The way to mitigate the presence of parasitic capacities among the windings is the incorporation of shields



In a similar way, by connecting a shield between windings the existing parasitic capacitance between primary and secondary is divided in two. The high frequency currents of the primary will find a low-impedance path through the capacitor and the screen being confined to the primary, thus preventing them from reaching the secondary winding.



The technology of resistance manufacturing conditions the parasitic elements of its equivalent model. If the resistance is coiled, the series inductance will have a high value, which limits its use in high frequency.

On the other hand, the carbon resistors have low series inductance, and their use in high frequency is possible